

DETAILED ACTION

Remarks

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after allowance or after an Office action under *Ex Parte Quayle*, 25 USPQ 74, 453 O.G. 213 (Comm'r Pat. 1935). Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, prosecution in this application has been reopened pursuant to 37 CFR 1.114. Applicant's submission filed on 7/21/2008 has been entered.
2. The amendment filed on 7/30/2008 under 37 CFR 1.312 has been entered.

Response to Arguments

3. Applicant's REMARKS filed 7/30/2008, with respect to the document presented on July 21, 2008 (hereinafter "the document") in an Information Disclosure Statement (hereinafter "the IDS") was fully considered. It was noted that the document is an internal document containing information belonging to Assignee, is not prior art, which was submitted in error. Accordingly, the IDS and the document were withdrawn from consideration. Furthermore, Applicant REMARKS/ARGUMENTS filed 7/30/2008, regarding typographical errors in the remarks in the Amendment after Allowance filed on July 21, 2008, was noted. Accordingly, the references to claims 25 and 26 in the

aforesaid amendment was discarded. Therefore the amendment to claim 9 has been made to correct a minor informality, has been entered.

Allowable Subject Matter

4. Claims 1-11 are allowed. The following is an examiner's statement of reasons for allowance:

The prior arts of record fail to teach or suggest in combination, the steps of **“receiving a stable high frequency reference signal; dividing said high frequency reference signal which allows a clock signal to have one of a plurality of clock phases; receiving pseudorandom number (PN) clock signal and providing PN phase adjustments of said received PN clock signal;”,** in combination with **“generating a tracking control signal in response to said PN phase adjustments for adjusting said system clock phase to one of the plurality of available phases; and adjusting said clock phase in accordance with the tracking control signal to provide said synchronized clock signal”** in order to form “A METHOD FOR PROVIDING A SYNCHRONIZED CLOCK SIGNAL HAVING REDUCED JITTER”, as recited in claim 1, and some variation of wording as recited in claims 6 and 9, and further limitation of their respective dependent claims 2-5, 7, 8, 10-12 correspondingly.

5. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Contact Information

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to NADER BOLOURCHI whose telephone number is (571)272-8064. The examiner can normally be reached on M-F.
7. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David. C. Payne can be reached on 571-272-3024. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.
8. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at (866) 217-9197 (toll-free).

/Kevin M. Burd/

Primary Examiner, Art Unit 2611

7/31/2008

/Nader Bolourchi/
Examiner, Art Unit 2611
7/30/2008

Application Number**Application/Control No.**

10/706,369

**Applicant(s)/Patent under
Reexamination**

KAEWELL, JOHN

Examiner

NADER BOLOURCHI

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